



Patent  
Attorney's Docket No. 027260-468

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

# Patent Application of

Michio KOMODA et al.

**Application No.: 09/879,197**

**Filed: June 13, 2001**

For: DELAY TIME ESTIMATION METHOD  
AND RECORDING...

Group Art Unit: 2123

**Examiner:** Unassigned

RECEIVED

SEP 19 2001

Technology Center 2100

## INFORMATION DISCLOSURE STATEMENT TRANSMITTAL LETTER

Assistant Commissioner for Patents  
Washington, D.C. 20231

**Sir:**

Enclosed is an Information Disclosure Statement and accompanying form PTO-1449 for the above-identified patent application.

- ☒ No additional fee for submission of an IDS is required.
- ☐ The fee of \$180.00 (126) as set forth in 37 C.F.R. § 1.17(p) is also enclosed.
- ☐ A certification under 37 C.F.R. § 1.97(e) is also enclosed.
- ☐ A certification under 37 C.F.R. § 1.97(e), and the fee of \$180.00 (126) as set forth in 37 C.F.R. § 1.17(p) are also enclosed.
- ☐ Charge \$\_\_\_\_\_ to Deposit Account No. 02-4800 for the fee due.
- ☐ A check in the amount of \$\_\_\_\_\_ is enclosed for the fee due.

The Commissioner is hereby authorized to charge any appropriate fees under 37 C.F.R. §§ 1.16, 1.17 and 1.21 that may be required by this paper, and to credit any overpayment, to Deposit Account No. 02-4800. This paper is submitted in duplicate.

Respectfully submitted,

**BURNS, DOANE, SWECKER & MATHIS, L.L.P.**

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By: Ellen Marcie Emas  
Registration No. 32,131

Date: September 17, 2001



To assist the Examiner, the documents are listed on the attached form PTO-1449. It is respectfully requested that an Examiner initialed copy of this form be returned to the undersigned.

Respectfully submitted,

BURNS, DOANE, SWECKER & MATHIS, L.L.P.

By: 

Ellen Marcie Emas  
Registration No. 32,131

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# INFORMATION DISCLOSURE STATEMENT BY APPLICANT

SEP 17 2001

**U.S. PATENT DOCUMENTS**

[illegible]

## FOREIGN PATENT DOCUMENTS

[illegible]

## NON PATENT LITERATURE DOCUMENTS

Examiner Initials	Include name of author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.		
	"Modeling the Driving-Point Characteristic of Resistive Interconnect for Accurate Delay Estimation", Proc. IEEE International Conference on Computer Aided Design, 1989, Peter R. O'BRIEN et al., IEEE		
	"A Gate-Delay Model for High-Speed CMOS Circuits", Proc. 31 <sup>st</sup> ACM/IEEE Design Automation Conference, 1994, Florentin DARTU et al., The University of Texas at Austin, Austin, Texas 78712		
	U.S. Application No. 09/878,352, Michio KOMODA et al., " Method of Producing Load for Delay Time Calculation and Recording Medium", June 12, 2001		
Examiner Signature		Date Considered	

(05/01)